IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of: Manfred Bartz and Marat Zhaksilikov

Serial No.: 10/010,591   Examiner:

Filed: 11/09/01   Art Unit:

For: MULTI-LEVEL QUICK CLICK ICON HIERARCHY AND/OR ACTIVATION

Assistant Commissioner for Patents
Washington, D.C.   20231

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Technology Center 2100

AMENDMENT TRANSMITTAL

1.   Transmitted herewith is an amendment for this application

  __X__ Transmitted herewith is a response to an office action for the above identified patent application.
     (__16__ sheets)
  __X__ Transmitted herewith are __1__ sheets of Red Line drawings.
  __X__ Other: 1 sheet of Drawings Amendment
  __X__ Other: 4 sheets of Supporting Documents

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2.   Applicant is other than a small entity

Extension of Term

3.   The proceedings herein are for a patent application and the provisions of 37 C.F.R. 1.136 apply.

(a)   [ ]   Applicant petitions for an extension of time under 37 C.F.R. 1.136
           (fees: 37 C.F.R. 1.17(a)-(d) for the total number of months checked below:)

Extension	Fee
[ ] one month	$110.00
[ ] two months	$400.00
[ ] three months	$920.00
[ ] four months	$1,960.00

                    Fee $ _____

If an additional extension of time is required, please consider this a petition therefor.

(b)   [X]   Applicant believes that no extension of term is required. However, this conditional petition is being made to provide for the possibility that applicant has inadvertently overlooked the need for a petition for extension of time.

F   Calculation

4.   The fee for claims (37 C.F.R. 1.16(b)-(d)) has been calculated as shown below:

(for other than a small ntity)					
Fee Items	Claims Remaining After Amendment	Highest Number of Claims Previously Paid For	Present Extra Claims	Fee Rate	Total
Total Claims	37	- 37 =	0	X $18.00	$0.00
Independent Claims	4	- 4 =	0	X $84.00	$0.00
Multiple Dependent Claim Fee (one or more, first added by this amendment)				$260.00	$0.00
Total Fees					$0.00

PAYMENT OF FEES

5.   The full fee due in connection with this communication is
provided as follows:

[X]   The Commissioner is hereby authorized to charge any additional fees associated with this communication or credit any overpayment to Deposit Account No.: 23-0085.
A duplicate copy of this authorization is enclosed.

[ ]   A check in the amount of $

[ ]   Charge any fees required or credit any overpayments associated with this filing to Deposit Account No.:   23-0085.

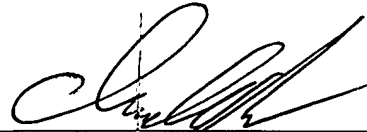Please direct all correspondence concerning the above-identified application to the following address:

WAGNER, MURABITO & HAO LLP
Two North Market Street, Third Floor
San Jose, California 95113
(408) 938-9060

Respectfully submitted,

Date: 12/24/2002   By: _____
                                            Anthony C. Murabito
                                            Reg. No. 35,295

PR-CD01163

IN THE UNITED STATES PATENT & TRADEMARK OFFICE

RE APPLICATION OF: :   ⚬

Manfred BARTZ et al.   : GROUP ART UNIT:

SERIAL NO: 10/010,591   :

FILED: NOVEMBER 9, 2001   : EXAMINER:

FOR: MULTI-LEVEL QUICK CLICK
       ICON HIERARCHY AND/OR
       ACTIVATION

PRELIMINARY AMENDMENT

COMMISSIONER FOR PATENTS
WASHINGTON, D.C. 20231

SIR:

Prior to examination of the above-identified application, consideration of the following

Amendments and Remarks is respectfully requested.

IN THE SPECIFICATION

Please replace the paragraph beginning at page 2, line 7 with the following new paragraph:

To overcome these problems, microcontroller suppliers (specifically, Cypress MicroSystems

in Bothell, Washington) have started to offer standard parts that combine a microprocessor with

several user-configurable "building blocks." These building blocks may be assembled, configured

and programmed to form many standard microprocessor peripherals, as well as to form unique

peripherals as may be required by a specific application. Thus, a user can tailor a microcontroller

to meet his or her specific requirements, in less time and at less cost than through other means. A

microcontroller assembled from these building blocks is referred to herein as a programmable single-

chip system. Additional information regarding such a microcontroller is provided in the co-pending,